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## 摘要

The main manufacturing processes of TFT-LCD consists of Array process, Cell process and Module process. In Array manufacturing, the equipment is expensive and the process is complex for bottleneck reentrant flows. The problem confronted by Array plant are the cycle time reduction plan and the throughput of improvement. The DBR management system in this paper is therefore proposed to improve the cycle time and throughput of an Array plant. The DBR management system (A-DBRMS) is first proposed and the modules are described detailed. A prototype is designed to demonstrate the feasibility of the proposed system. An eM-Plant simulation model is final utilized to test the function provided by this prototype.

關鍵字:TFT-LCD、Array Manufacture、Bottleneck reentrant flows、Drum-Buffer-Rope.